



CALL FOR PAPERS

ROCS Workshop

Reliability Of Compound Semiconductors

Monday May 22, 2017

Preceding the International Compound Semiconductor MANufacturing TECHNOLOGY Conference

Hyatt Regency Indian Wells Resort & Spa

44600 Indian Wells Lane,

Palm Springs, (Indian Wells), California, USA

SPONSORED BY JEDEC COMMITTEE JC-14.7

The 32nd annual **Reliability Of Compound Semiconductors Workshop** will be held one day before the Compound Semiconductor MANufacturing TECHNOLOGY Conference with the objective of bringing together researchers, manufacturers, & users of compound devices. Papers presenting the latest results and new developments in all phases of Compound Semiconductor Reliability are solicited. Original papers discussing work in progress and emerging device technologies are acceptable and encouraged. Topics of interest include:

Failure Mechanisms
Metallization Studies
Electromigration
Materials Defects
Drift, Backgating
ESD/EOS/Pulse Burnout

Circuit Reliability
Cost of Reliability
Quality Assurance
Reliability Standards
Ionic Migration
Hot Electron Effects

Reliability Modeling
Thermal Analysis
Accelerated Testing
Radiation Effects
Novel Device Problems
Environmental Effects

PAPER SUBMISSION: Authors are asked to submit electronic copies of a one or two page comprehensive summary suitable for a 15 minute presentation. The summary must clearly state: **(1) the purpose of the work and what problems or questions it addressed, (2) the results or conclusions of the work, and (3) how the work advances the knowledge of Compound Semiconductor Reliability.**

The authors' name, affiliation, complete address, telephone number, FAX number, and E-mail must appear on the summary. Authors are responsible for obtaining all required company and government clearances prior to submission of the summary. Summaries must be emailed to rocs@jedec.org by **February 27, 2017**. Acceptance notices will be sent out by March 20, 2017. Upon acceptance, authors will be asked to submit one camera-ready original of an extended summary, viewgraphs, and a copyright release by April 17, 2017 for publication and distribution to the attendees at the workshop. All submittals become the property of the **ROCS Workshop**, and will not be returned.

SUMMARIES MUST BE SUBMITTED BY February 27, 2017

ADVANCE REGISTRATION IS REQUIRED TO ASSURE PARTICIPATION IN THE LUNCHEON

Save money and pre-register, please check our website for the latest registration details.

Visit our Website at... <http://www.jedec.org/home/gaas/>

2017 ROCS Workshop

Monday, May 22, 2017 - Indian Wells (Palm Springs) CA

To submit a paper

Send Summaries, by February 27, 2017 to: rocs@jedec.org

electronic submittal is required, PDF files are okay, or use Microsoft Word as a default

ROCS Workshop Committee Members:

WORKSHOP CHAIRMAN

Martin Kuball
University of Bristol

TECHNICAL PROGRAM CO-CHAIRMEN

Randy Lewis
Northrop Grumman
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Western Australia

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MACOM

ADVANCE REGISTRATION MUST BE ENTERED ON-LINE BY MAY 8, 2017

Registration for the 2017 ROCS Workshop includes:

Full Day of Compound Semiconductor Reliability Presentations

A Copy of the Digest of Papers

Luncheon and 2 Breaks

Advance Registration = \$225 (on-line)

On-site Registration = \$275

Special (Advance Only) Registration Fee of \$200 for JEDEC Members.

Special \$100 Advance Registration Fee for Students (Presenting Student Authors get in Free, Advance Registration Only).

At-the-door registration, as available, will be at Indian Wells on May 22nd from 7:30-8:30AM

For further information, contact:

Martin Kuball
University of Bristol
Workshop Chairman

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