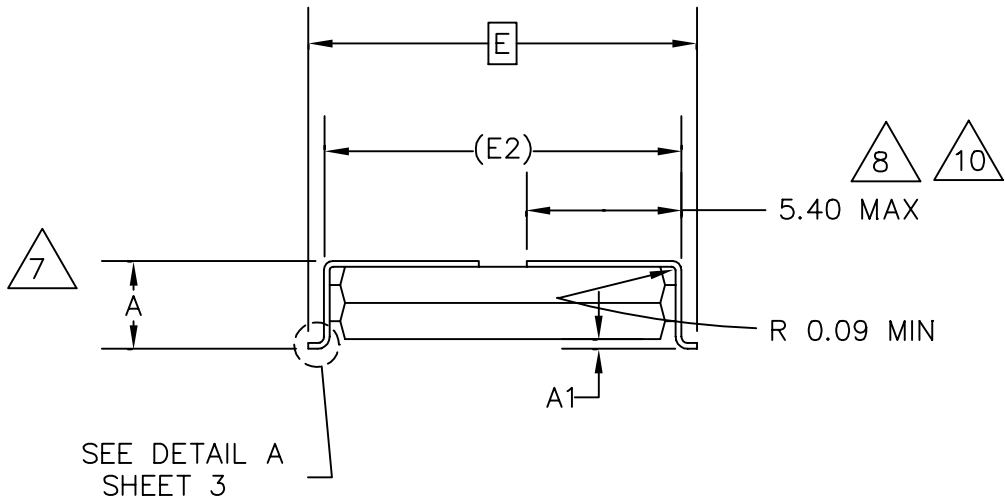


PATENT CLAIMS

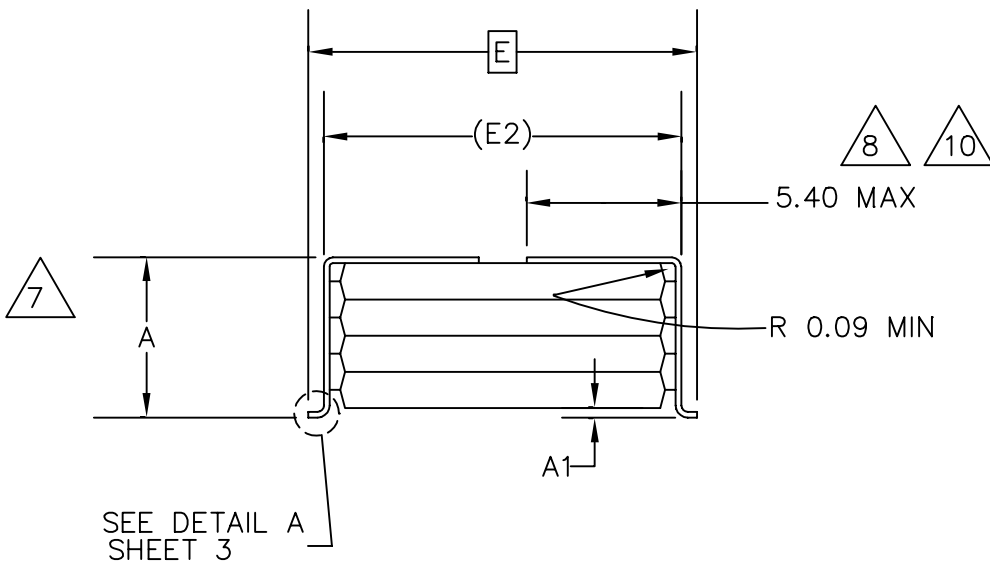
MOLEX CORPORATION HAS STATED THAT U.S. PATENT NUMBERS 5377077, 5420751, AND 5446620 ASSIGNED TO STAKTEK CORPORATION MAY RELATE TO A CERTAIN IMPLEMENTATION OF THIS PACKAGE OUTLINE.

<p>JEDEC SOLID STATE PRODUCT OUTLINES</p>	<p>THIS REGISTERED OUTLINE HAS BEEN PREPARED BY THE JEDEC JC-11 COMMITTEE AND REFLECTS A PRODUCT WITH ANTICIPATED USAGE IN THE ELECTRONICS INDUSTRY; CHANGES ARE LIKELY TO OCCUR.</p>				
<p>TITLE 2HIGH/4HIGH TSOP II</p>	<p>PDSO-G/TSOP II</p>	<p>ISSUE A</p>	<p>DATE OCT 97</p>	<p>MO-201</p>	<p>SHEET 1 OF 4</p>

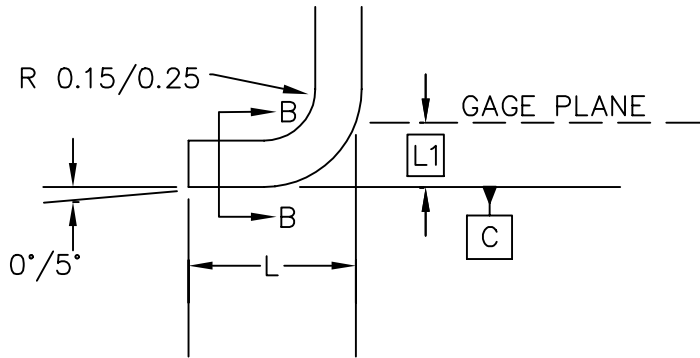
VARIATION AA



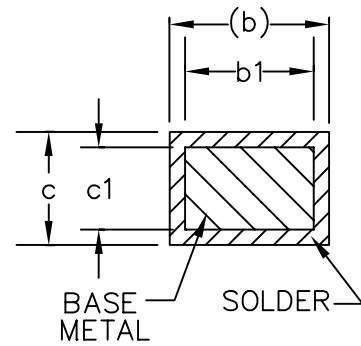
VARIATION AB



DETAIL A



SECTION B-B



SYMBOL	ALL DIMENSIONS IN MILLIMETERS						NOTE
	VARIATION AA			VARIATION AB			
	MIN	NOM	MAX	MIN	NOM	MAX	
A	-	-	2.50	-	-	5.00	7
A1	0.05	0.20	0.30	0.05	0.20	0.30	-
b	0.35	-	0.52	0.35	-	0.52	6
b1	0.35	0.40	0.45	0.35	0.40	0.45	6
b2	-	-	0.60	-	-	0.60	-
c	0.15	-	0.25	0.15	-	0.25	-
c1	0.15	0.18	0.20	0.15	0.18	0.20	-
D	20.95 BASIC			20.95 BASIC			4
E	11.76 BASIC			11.76 BASIC			-
E1	10.16 BASIC			10.16 BASIC			5
E2	(11.15 REF)			(11.15 REF)			-
e	1.27 BASIC			1.27 BASIC			-
L	0.50	0.65	0.75	0.50	0.65	0.75	-
L1	0.25 BASIC			0.25 BASIC			-
N	32			32			9
NOTE	1, 2			1, 2			
REF	11-429			11-429			
ISSUE	A			A			

NOTES:

1. DIMENSIONS AND TOLERANCES CONFORM TO ASME Y14.5M-1994.
2. ALL DIMENSIONS ARE IN MILLIMETERS.
3. THE LEAD #1 IDENTIFIER AND THE LEAD NUMBERING CONVENTION SHALL CONFORM TO JESD 95-1 SPP-012. DETAILS OF THE LEAD #1 IDENTIFIER ARE OPTIONAL BUT MUST BE LOCATED WITHIN THE ZONE INDICATED. THE LEAD #1 IDENTIFIER MAY BE EITHER A MOLDED OR A MARKED FEATURE.
4. DIMENSION D DOES NOT INCLUDE MOLD PROTRUSIONS OR GATE BURRS. MOLD PROTRUSIONS AND GATE BURRS SHALL NOT EXCEED 0.15 MM PER SIDE.
5. DIMENSION E1 DOES NOT INCLUDE INTERLEAD MOLD PROTRUSIONS. INTERLEAD MOLD PROTRUSIONS SHALL NOT EXCEED 0.25 MM PER SIDE.
6. THESE DIMENSIONS APPLY TO THE FLAT SECTION OF THE LEAD BETWEEN 0.10 MM AND 0.25 MM FROM THE LEAD TIP.
7. HEIGHT VARIES WITH THE NUMBER OF COMPONENTS PER MODULE, AND THE METHOD OF STACKING. ALTERNATE METHODS OF STACKING ARE PERMISSIBLE.
8. THIS FEATURE IS OPTIONAL.
9. N IS THE NUMBER OF LEADS.
10. RAIL-TO-LEAD INTERCONNECT DESIGN IS OUTSIDE THE SCOPE OF THIS OUTLINE.

JEDEC SOLID STATE PRODUCT OUTLINES	TITLE PDSO-G 2HIGH/4HIGH TSOP II	ISSUE A	DATE OCT 97	MO-201	SHEET 4 OF 4
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