

3.9 Dynamic Random Access Memory (DRAM)

The following DRAM standards were developed by Committee 42.3 and have been implemented using MOS technology.

3.9.1 Bit Wide DRAM

3.9.2 Nibble Wide DRAM

3.9.3 Byte Wide DRAM

3.9.4 Word Wide DRAM

3.9.5 DRAM Optional Features