

3.7.2 Bit Wide ECL SRAM

All of the following standards are for devices which operate with ECL interface levels and power voltages.

3.7.2.1 – 1K TO 256K BY 1 ECL SRAM FAMILY IN DIP

CAPACITY—1K, 4K, 16K, 64K & 256K WORDS OF 1 BIT
ELECTRICAL INTERFACE—10K or 100K ECL
FAMILY—1K, 4K, 16K, & 256K = 10K & 100K ECL
—64K = 10K ECL
PACKAGE—16, 18, 20 OR 22 DIP, 0.4" WIDE
PIN ASSIGNMENT—Fig. 3.7.2-1

■ **3.7.2.2 – 256K TO 16M BY 1 ECL SRAM AND 4M BY 1 SSRAM IN DIP, SOJ, AND TSOP-2**

CAPACITY—256K, 1M, 4M, AND 16M WORDS OF 1 BIT
ELECTRICAL INTERFACE—10K or 100K ECL
LOGIC FEATURES—OPTIONAL OUTPUT ENABLE FOR SOME DENSITIES
—SEPARATE DATA INPUT & OUTPUT PINS
—4M DENSITY PART APPROVED AS SYNCHRONOUS SRAM
CAPACITY—256K, 1M, 4M, AND 16M WORDS OF 1 BIT
PACKAGE—28 or 32 PIN SOJ, & TSOP2, 0.3", 0.4", or 0.5" WIDE with PP=0.05"
—28 Or 32 PIN DIP, 0.3", 0.4" with PP=0.1", or 0.6" with PP=0.07".
SPECIAL FEATURES—MULTIPLE CENTERED POWER PINS
PIN ASSIGNMENT—Fig. 3.7.2-2

3.7.2.3 – 64K AND 256K BY 1 ECL SRAM IN FLATPACK

CAPACITY—64K, & 256K WORDS OF 1 BIT
ELECTRICAL INTERFACE—10K or 100K ECL
PACKAGE—22, OR 24 PIN FP, 0.535" WIDE, 0.030 LEAD PITCH
PIN ASSIGNMENT—Fig. 3.7.2-3

■ **3.7.2.4 – 256K TO 16M BY 1 ECL SSRAM FAMILY IN DIP, SOJ, AND TSOP-2**

CAPACITY—256K, 1M, 4M, & 16M WORDS OF 1 BIT
ELECTRICAL INTERFACE—10K or 100K ECL
PACKAGE—28, 32, or 36 PIN SOJ, & TSOP2, 0.3", 0.4", or 0.5" wide, PP=0.05"
—28 Or 32 PIN DIP, 0.3", 0.4" with PP=0.1", or 0.6" with PP=0.07".
SPECIAL FEATURES—MULTIPLE CENTERED POWER PINS
—DIFFERENTIAL CLOCKS
PIN ASSIGNMENT—Fig. 3.7.2-4

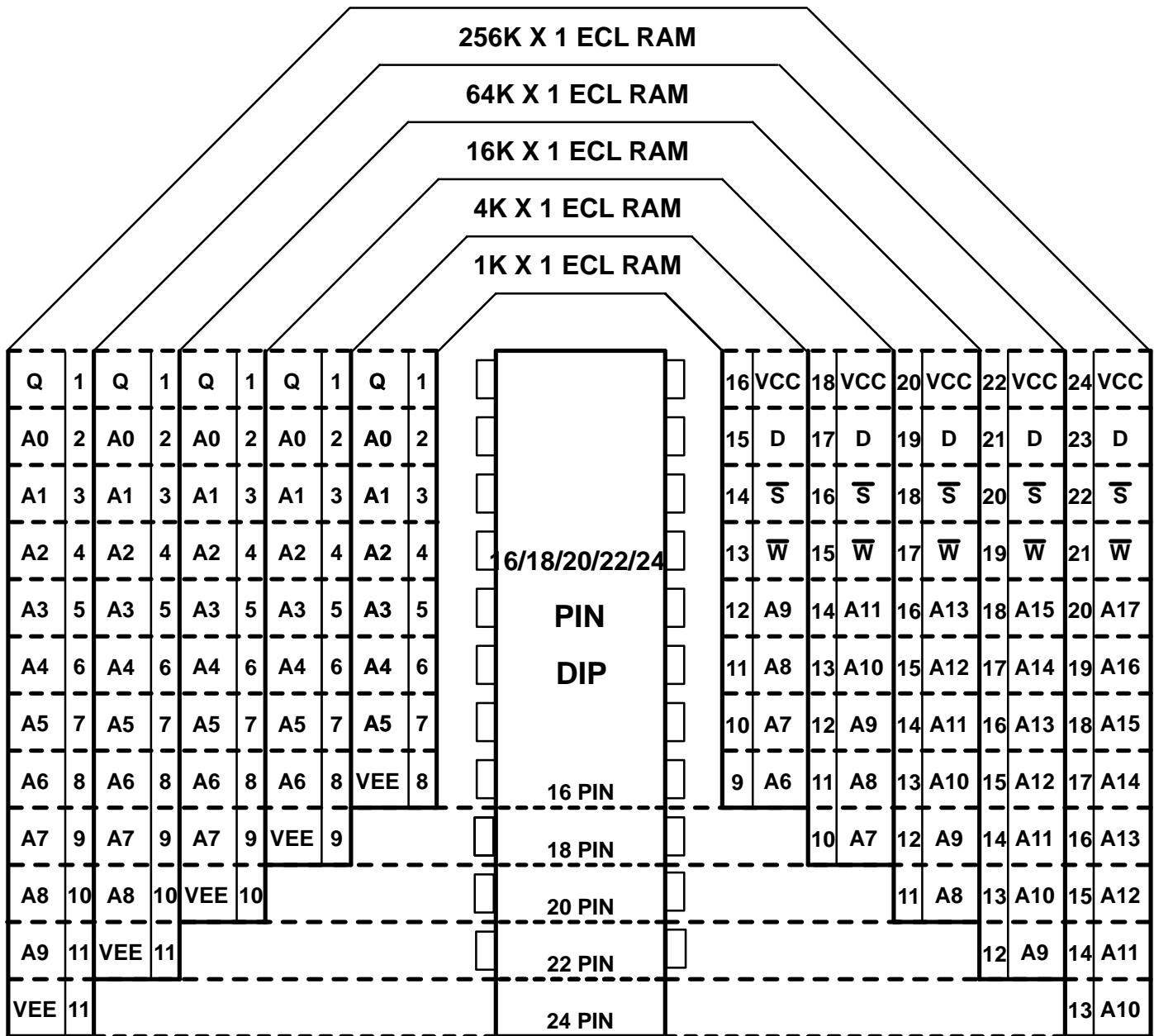
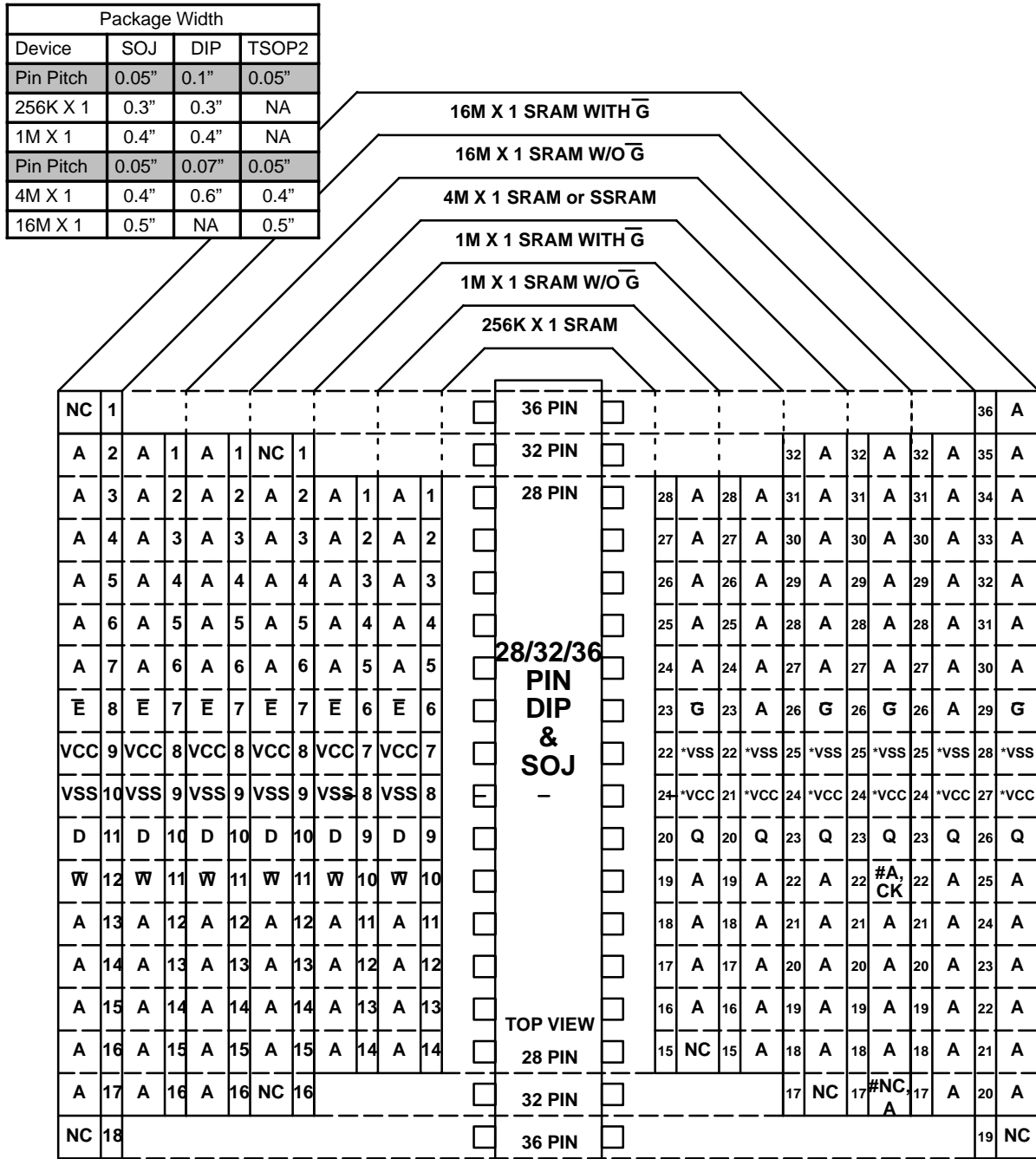


FIGURE 3.7.2-1
1K TO 256K BY 1 ECL SRAM FAMILY IN DIP



* These power pins may be VCC OR VCCA (VSS OR VSSA) as a Manufacturer option

NOTE: For the 4M X 1 devices, the pin assignments for pins 17 & 22 are different for the SRAM and SSRAM as follows.
For SRAM, P17 = NC, P22 = A
For SSRAM, P17 = A, P22 = CK

FIGURE 3.7.2-2

256K TO 16M BY 1 ECL SRAM AND SSRAM FAMILY IN DIP, TSOP2, & SOJ

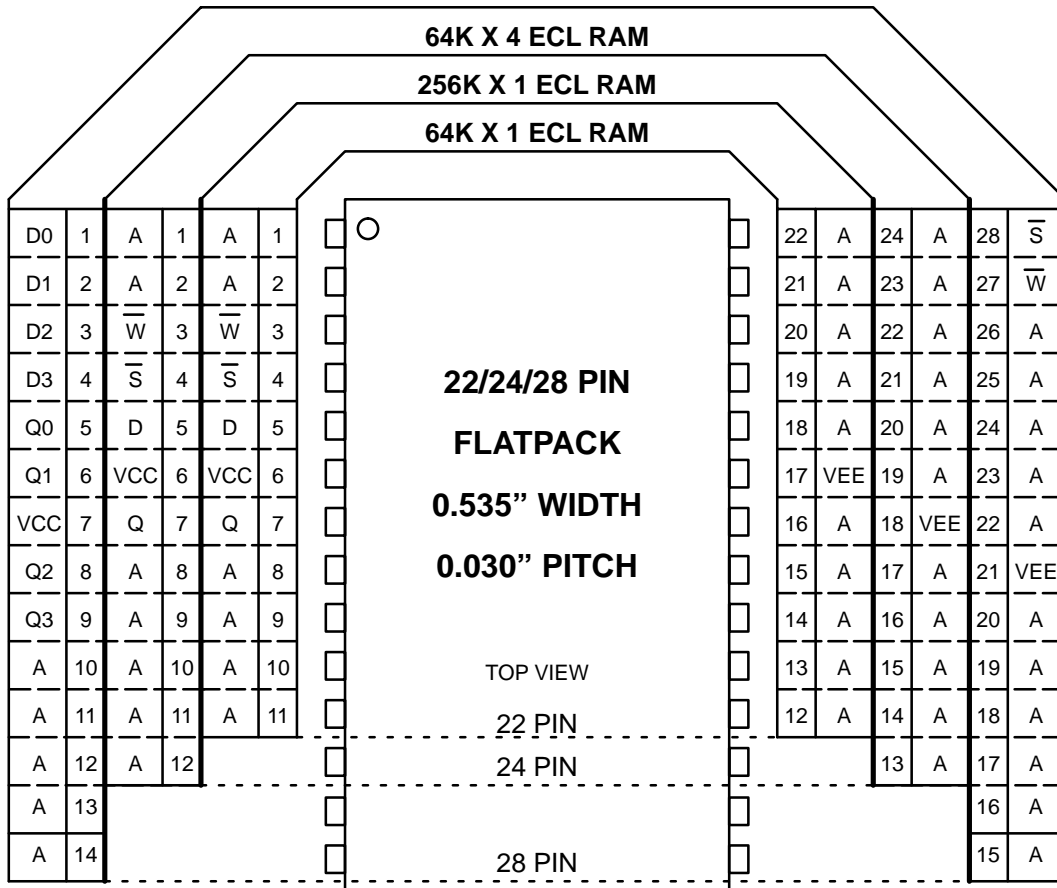
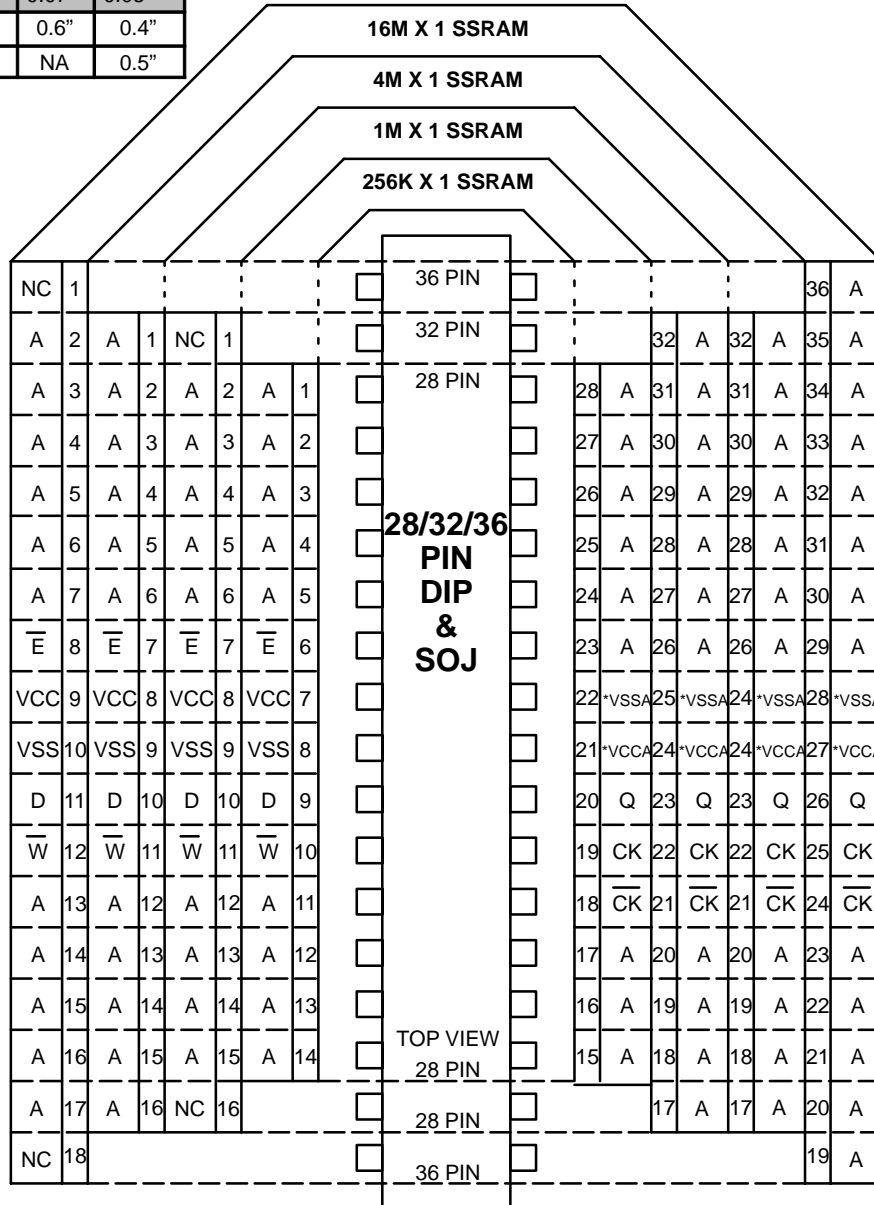


FIGURE 3.7.2-3
64K AND 256K BY 1 ECL SRAM IN FP

| Package Width | | | |
|---------------|-------|-------|-------|
| Device | SOJ | DIP | TSOP2 |
| Pin Pitch | 0.05" | 0.1" | 0.05" |
| 256K X 1 | 0.3" | 0.3" | NA |
| 1M X 1 | 0.4" | 0.4" | NA |
| Pin Pitch | 0.05" | 0.07" | 0.05" |
| 4M X 1 | 0.4" | 0.6" | 0.4" |
| 16M X 1 | 0.5" | NA | 0.5" |



* These power pins may be VCC OR VCCA (VSS OR VSSA) as a Manufacturer option

FIGURE 3.7.2-4
256K TO 16M BY 1 ECL SSRAM FAMILY IN DIP, SOJ, & TSOP2