

3.4.2 EPROM, Word Wide

3.4.2.1 – 32K TO 256K BY 16 EPROM IN DIP

CAPACITY—32K TO 256K WORDS OF 16 BITS,
PACKAGE—40 PIN DIP, 0.6" WIDE
PIN ASSIGNMENTS—Fig. 3.4.2-1

3.4.2.2 – 32K TO 256K BY 16 EPROM IN SCC

CAPACITY—32K TO 256K WORDS OF 16 BITS,
PACKAGE—44 PAD (PIN) RCC, 0.650" X 0.650"
PIN ASSIGNMENTS—Fig. 3.4.2-2

3.4.2.3 – 16K TO 256K BY 16 ADDRESS/DATA MX EPROM IN DIP

CAPACITY—16K TO 256K WORDS OF 16 BITS,
LOGIC FEATURES—Address and Data MULTIPLEXED onto common pins.
PACKAGE—28 PIN DIP, 0.6" WIDE
PIN ASSIGNMENTS—Figs. 3.4.2-3

3.4.2.4 – 16K TO 256K BY 16 ADDRESS/DATA MX EPROM IN RCC

CAPACITY—16K TO 256K WORDS OF 16 BITS,
LOGIC FEATURES—Address and Data MULTIPLEXED onto common pins.
PACKAGE—32 PIN (PAD) RCC, 0.450" X 0.550"
PIN ASSIGNMENTS—Figs. 3.4.2-4

3.4.2.5 – 512K TO 128M BY 16 EPROM IN DIP AND SOP

CAPACITY—512K, 1M, 2M, 4M, 8M, 16M, 32M, 64M, 128M WORDS OF 16 BITS
PACKAGE—42, 44, 48 and 52 PIN DIP or SOP, 0.6" WIDE
PIN ASSIGNMENTS—512K TO 2M, Fig. 3.4.2-8
PIN ASSIGNMENTS—4M TO 128M, Fig. 3.4.2-5

3.4.2.6 – 512K and 1M BY 16 EPROM IN SCC

CAPACITY—512K & 1M WORDS OF 16 BITS
PACKAGE—44TERMINAL SCC, 0.650" X 0.650"
PIN ASSIGNMENTS—Fig. 3.4.2-6

3.4.2.7 – 64K TO 256K BY 16 EPROM IN TSOP-1

CAPACITY—64K, 128K & 256K WORDS OF 16 BITS
PACKAGE—48 PIN TSOP-1 12 mm X 20 mm, PP=0.5 mm
PIN ASSIGNMENTS—Fig. 3.4.2-7

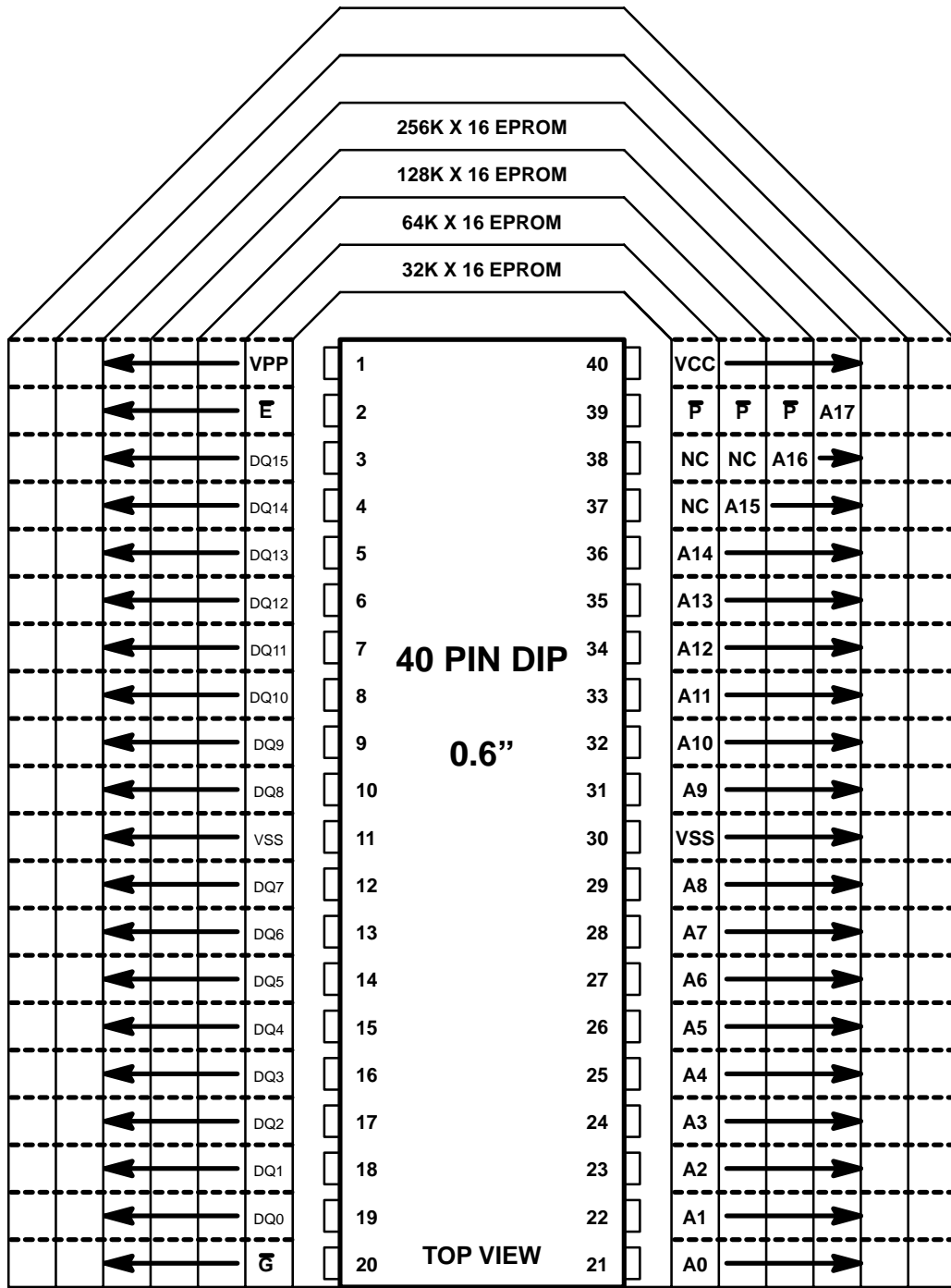
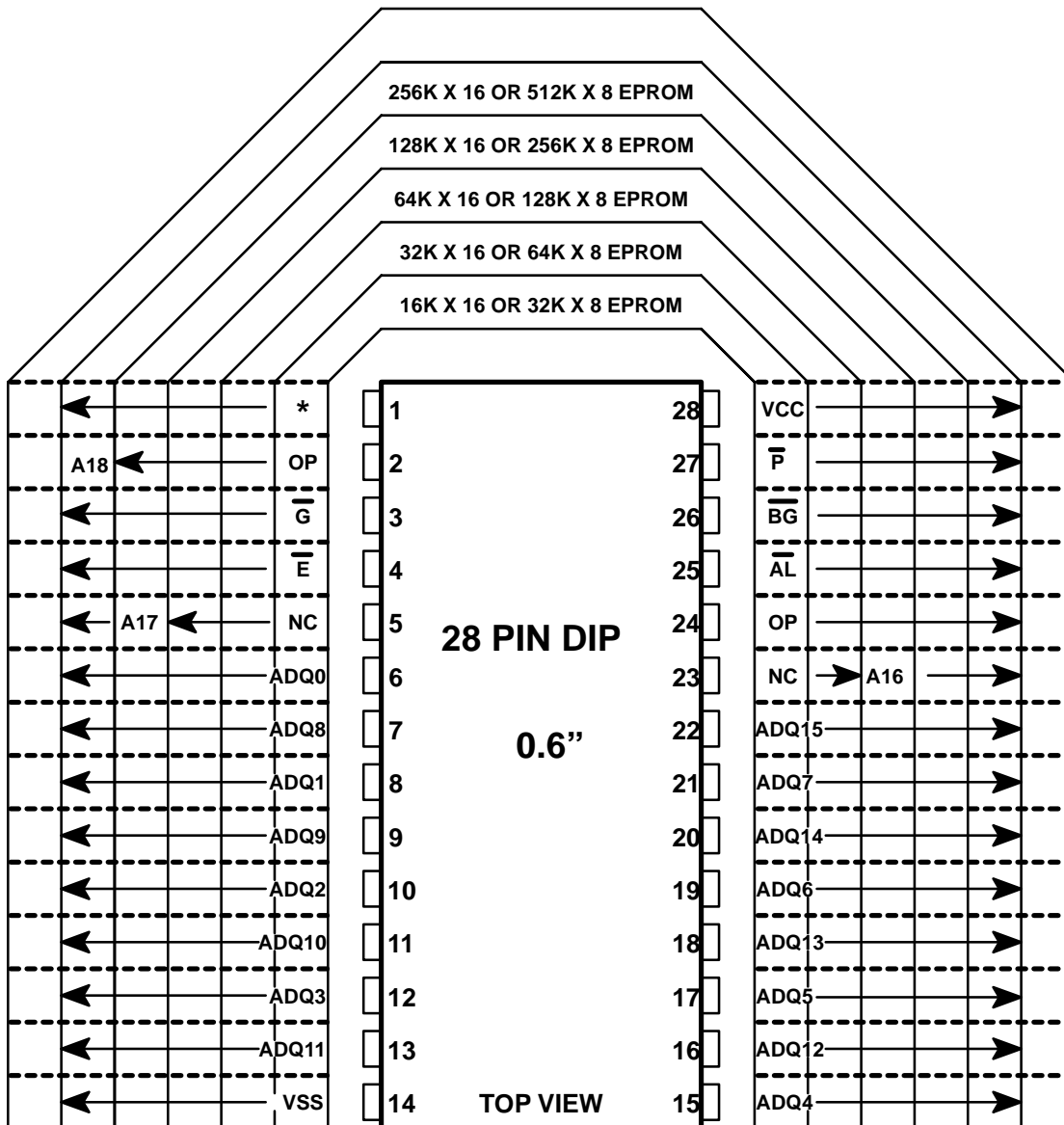


FIGURE 3.4.2-1
32K TO 256K BY 16 EPROM IN DIP

$\overline{\text{BG}}$	A0	D0-D7	D8-D15	OPERATION
L	L	LOWER BYTE	UPPER BYTE	WORD
L	H		UPPER BYTE	
H	L	LOWER BYTE		BYTE
H	H	UPPER BYTE		BYTE



* NC or VSS

FIGURE 3.4.2-3
16K TO 256K BY 16 ADDRESS/DATA MX EPROM IN DIP

$\overline{\text{BG}}$	A0	D0-D7	D8-D15	OPERATION
L	L	LOWER BYTE	UPPER BYTE	WORD
L	H		UPPER BYTE	
H	L	LOWER BYTE		BYTE
H	H	UPPER BYTE		BYTE

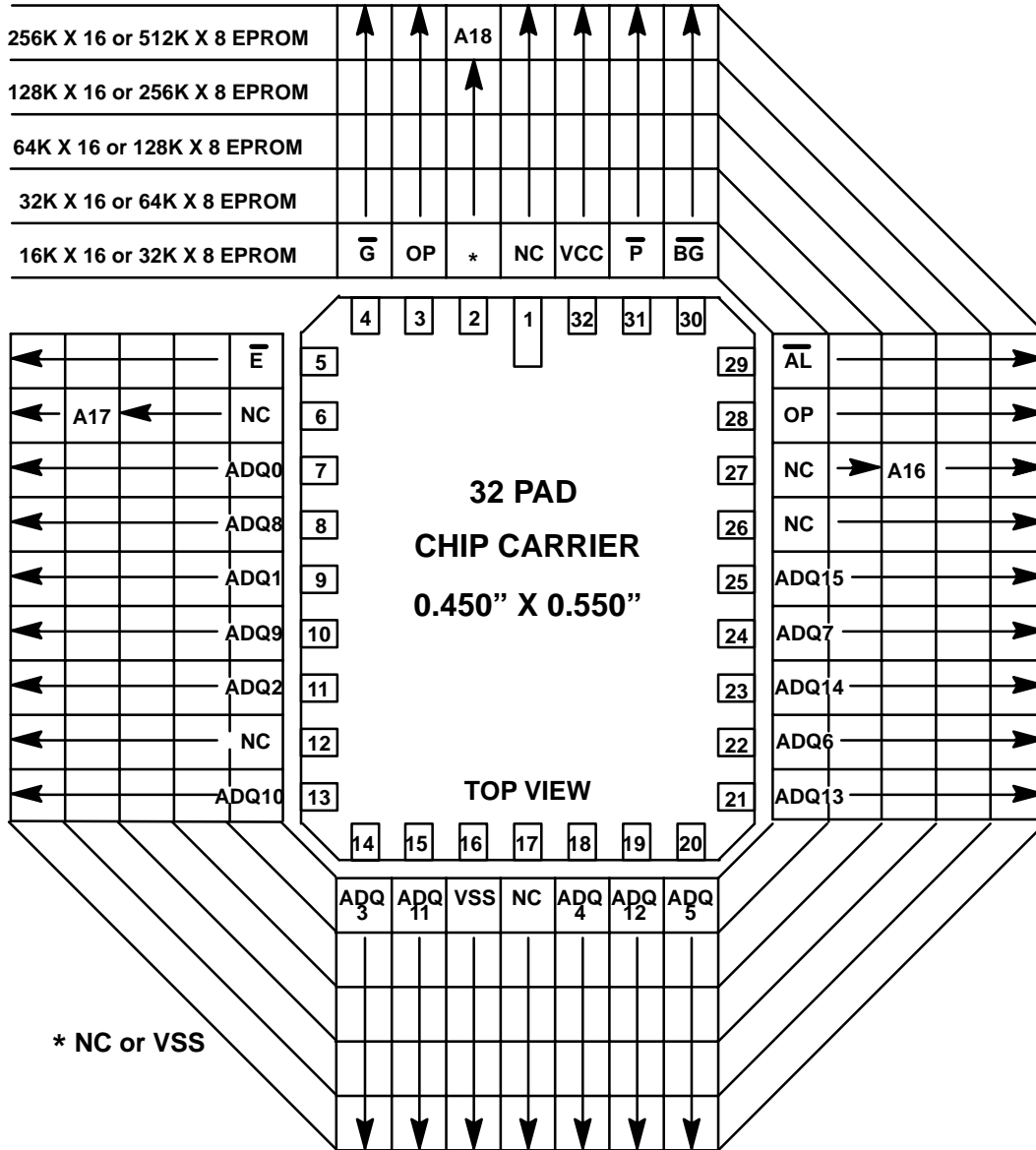
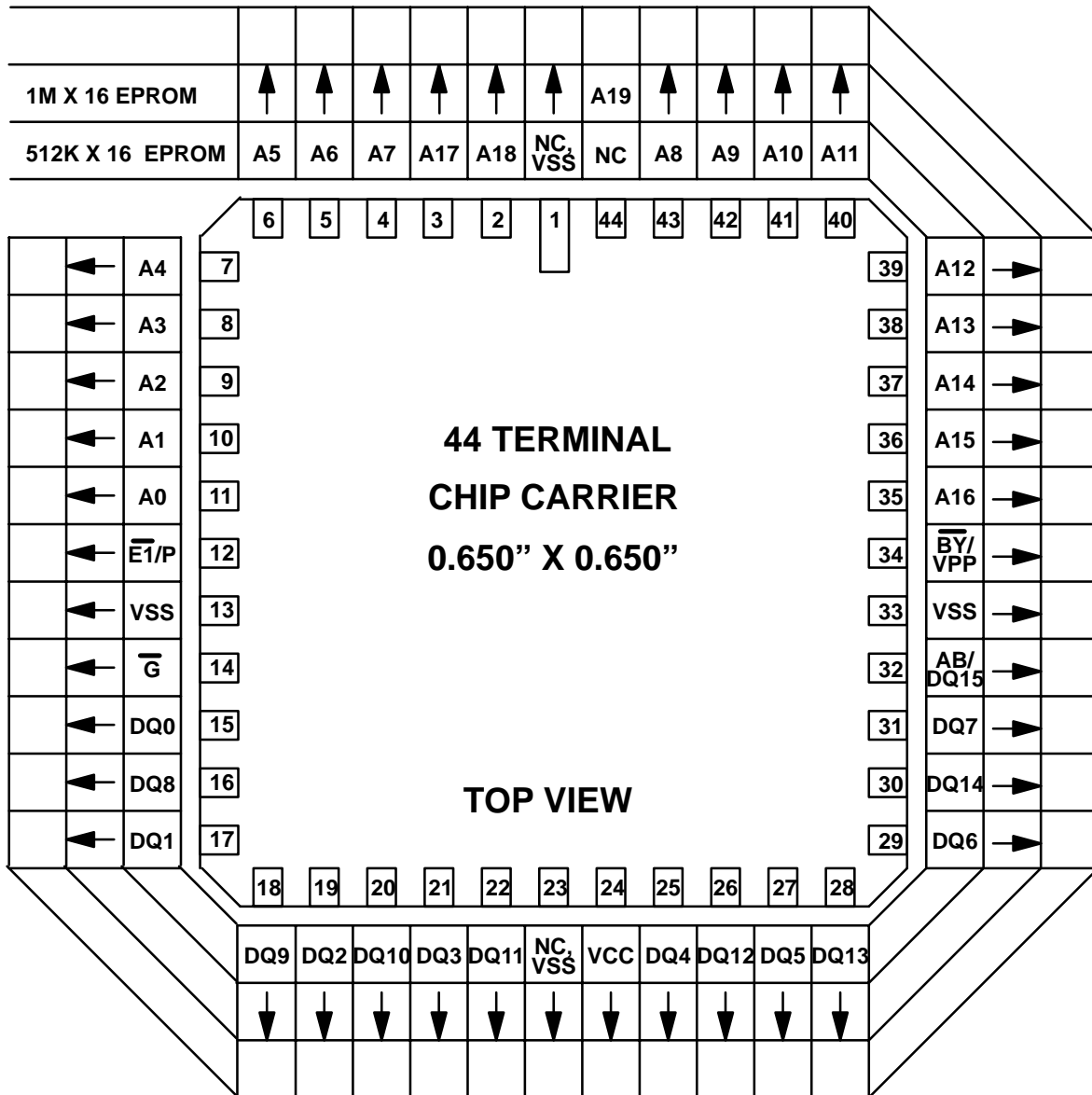


FIGURE 3.4.2-4
16K TO 256K BY 16 ADDRESS/DATA MX EPROM IN RCC



The device is x8/x16 programmable via BY (pin 34)
 X8 configuration when BY = VIL
 X16 configuration when BY = VIH
 AB is the least significant address bit for X8 operation

**FIGURE 3.4.2-6
 512K & 1M BY 16 EPROM IN CC**

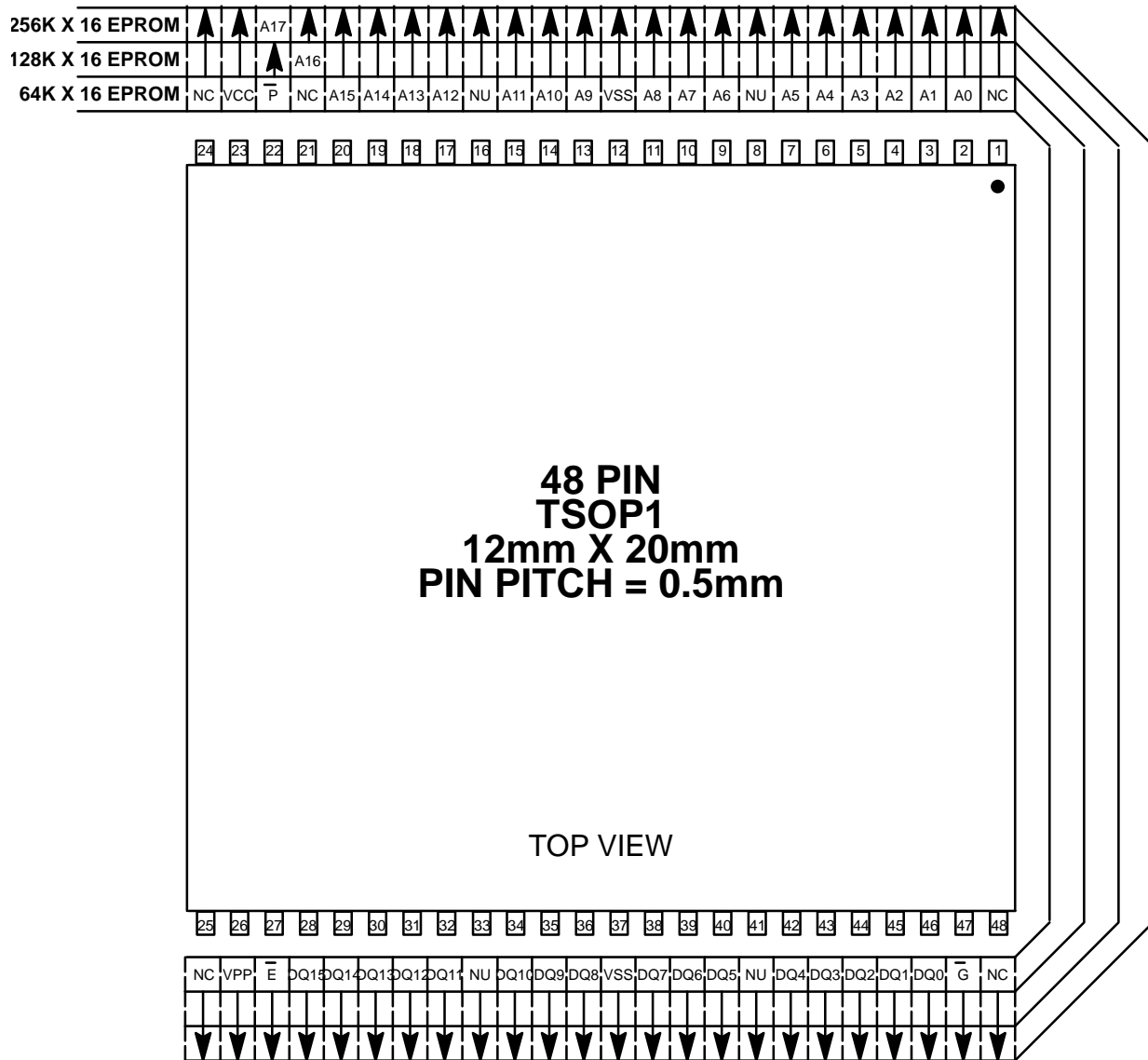
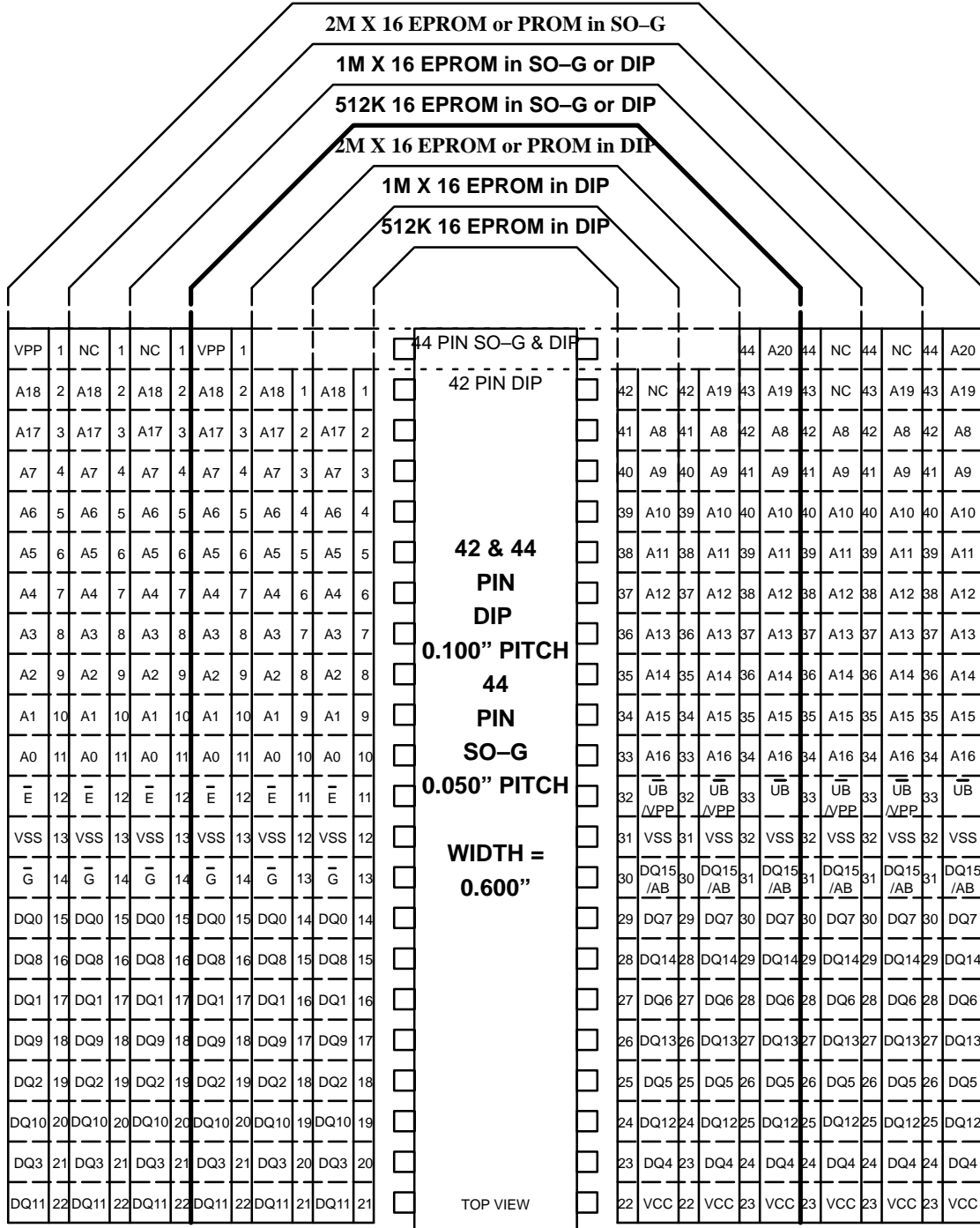


FIGURE 3.4.2-7
64K BY 256K BY 16 EPROM IN TSOP-1



AB: ADDRESS INPUT, UPPER/
LOWER BYTE SELECT
BT: BYTE MODE ENABLE

**FIGURE 3.4.2-8
512K TO 2M BY 16 EPROM IN DIP AND SO-G**